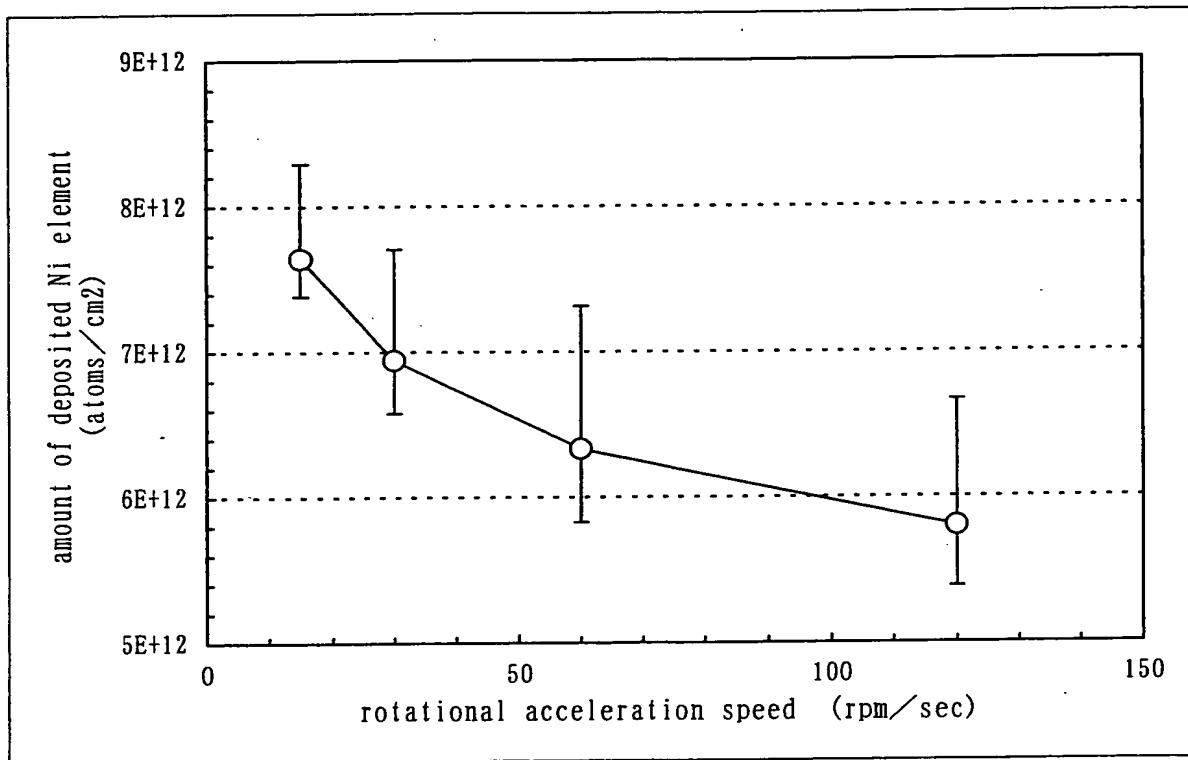
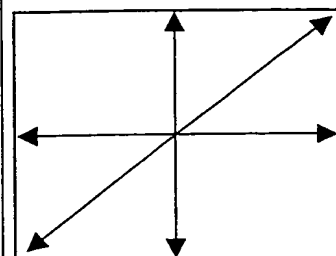
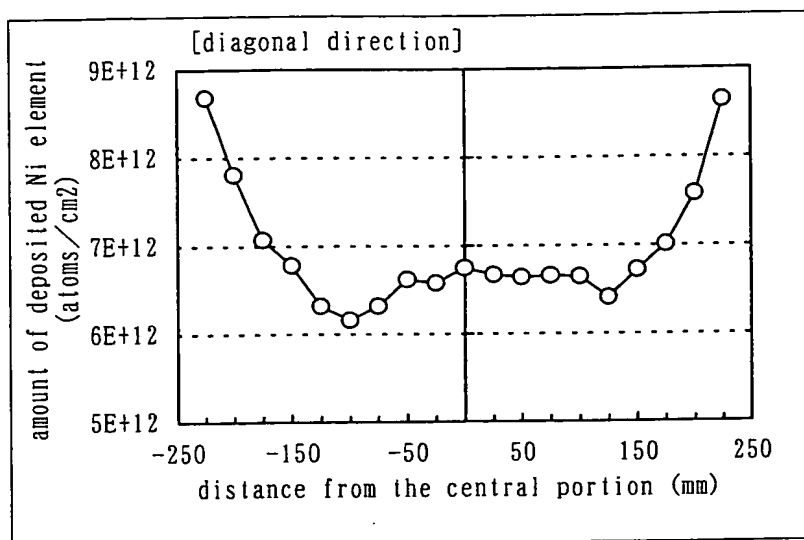


FIG. 1





measure direction within the substrate

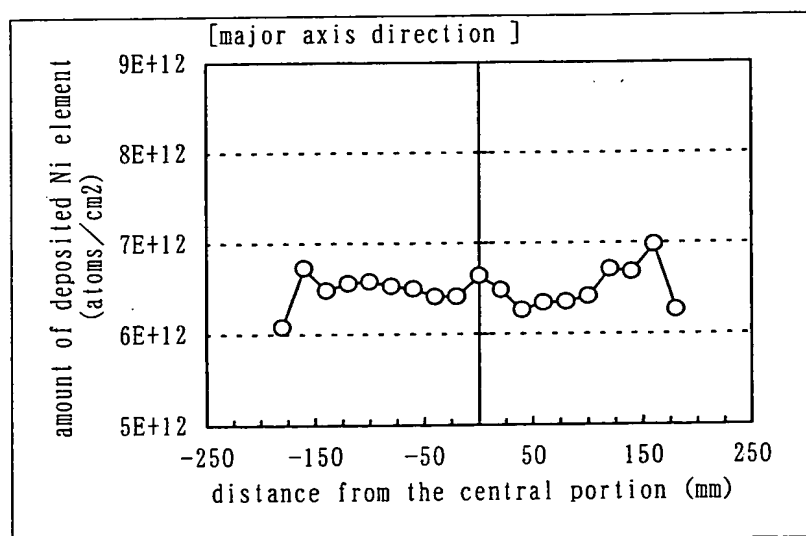
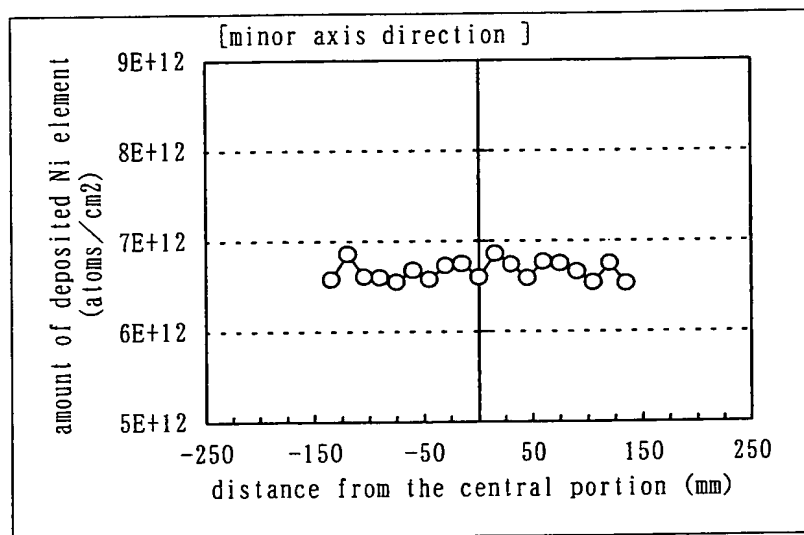
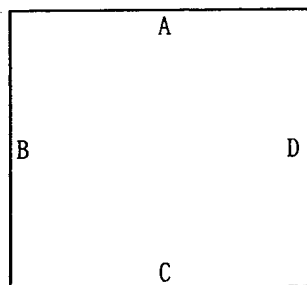
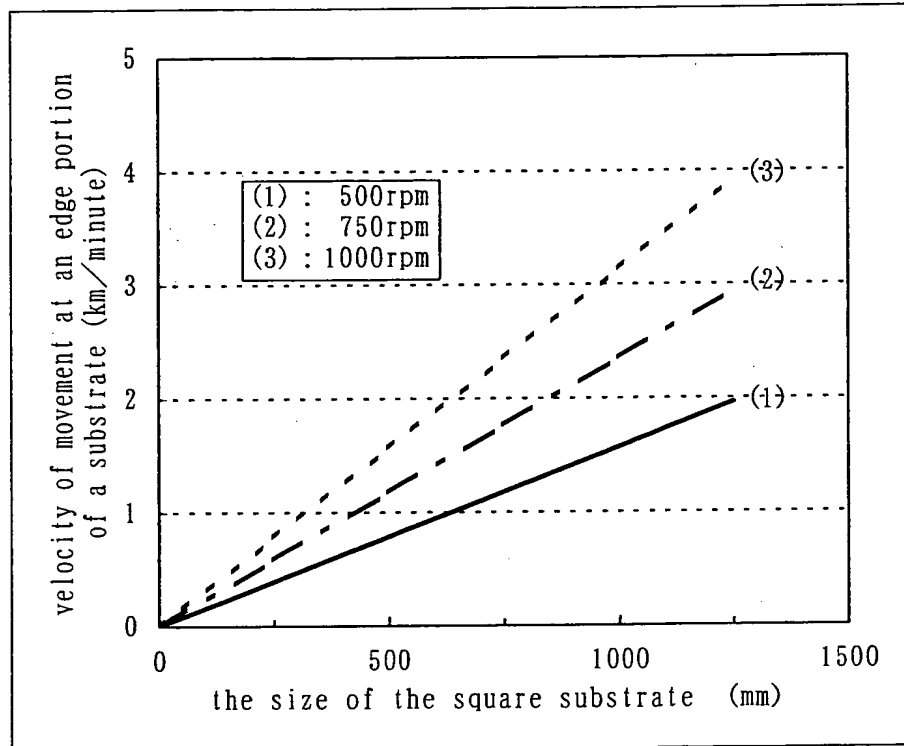


FIG. 2

FIG. 3



Edge portions of the substrate correspond to A-D in the square substrate.

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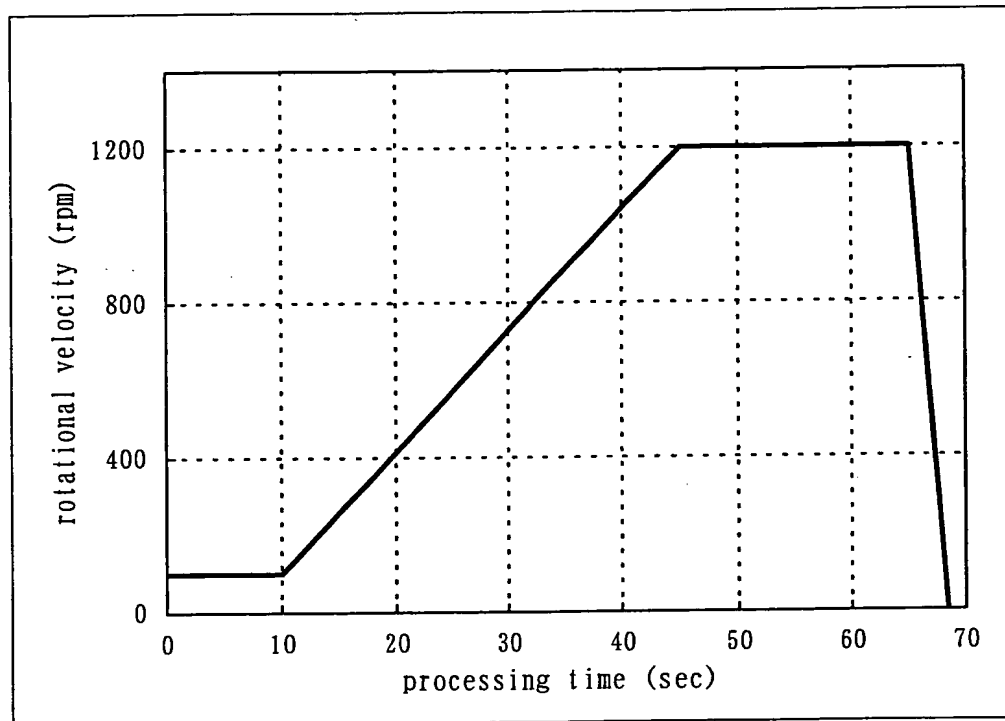
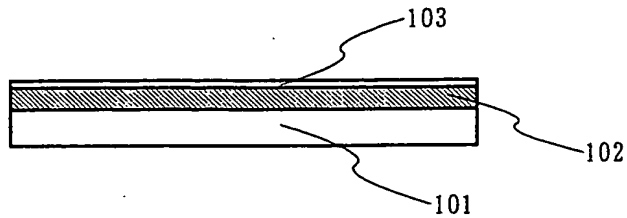


FIG. 5A



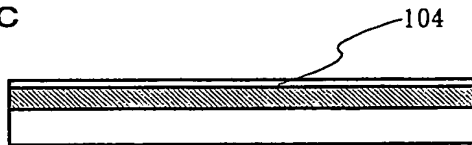
DEPOSITION OF AMORPHOUS SILICON FILM

FIG. 5B



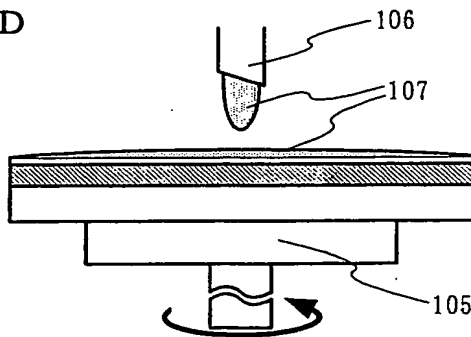
REMOVAL OF NATURAL OXIDE FILM BY DILUTE HYDROFLUORIC ACID PROCESSING

FIG. 5C



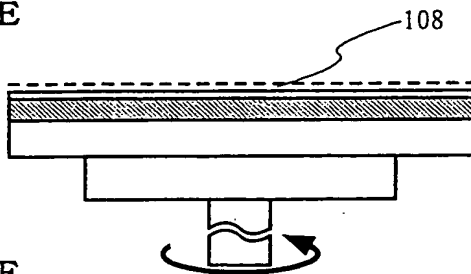
FORMATION OF EXTREMELY THIN SILICON OXIDE FILM BY AQUEOUS OZONE PROCESSING

FIG. 5D



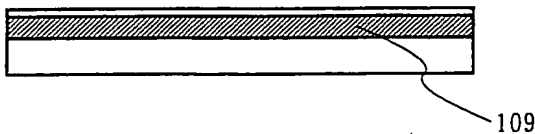
ADDITION OF AQUEOUS NI ELEMENT SOLUTION BY SPIN ADDITION METHOD SPIN ADDITION IN LOW VELOCITY SPIN STATE OF 100 RPM

FIG. 5E



FORMATION OF Ni-CONTAINING LAYER BY SPIN DRYING ACCELERATION TO 1200 RPM AT LOW ACCELERATION OF 30 RPM/SEC SPIN DRYING FOR 20 SEC AT 1200 RPM

FIG. 5F



CRYSTALLIZATION OF AMORPHOUS SILICON FILM BY HEAT TREATMENT (VERTICAL GROWTH METHOD)

FIG. 6A

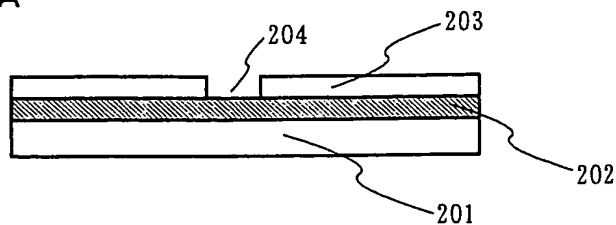


FIG. 6B

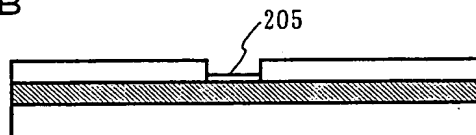


FIG. 6C

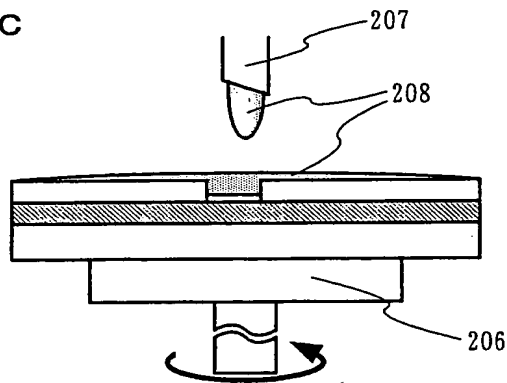


FIG. 6D

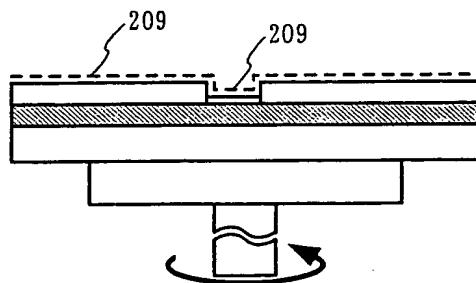
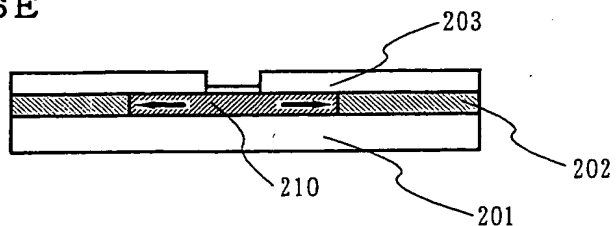


FIG. 6E



DEPOSITION OF AMORPHOUS SILICON FILM

DEPOSITION OF MASK INSULATING FILM

FORMATION OF OPENING REGION

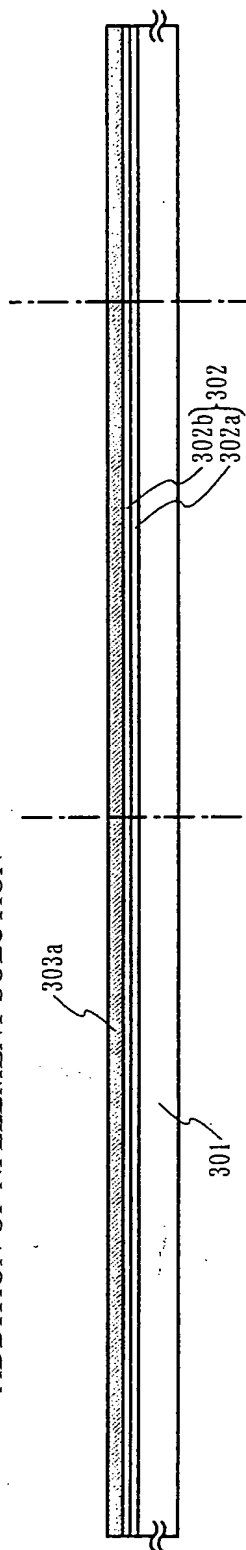
FORMATION OF EXTREMELY THIN SILICON OXIDE FILM IN OPENING REGION

ADDITION OF AQUEOUS Ni ELEMENT SOLUTION BY SPIN ADDITION METHOD SPIN ADDITION IN LOW VELOCITY SPIN STATE OF 100 RPM

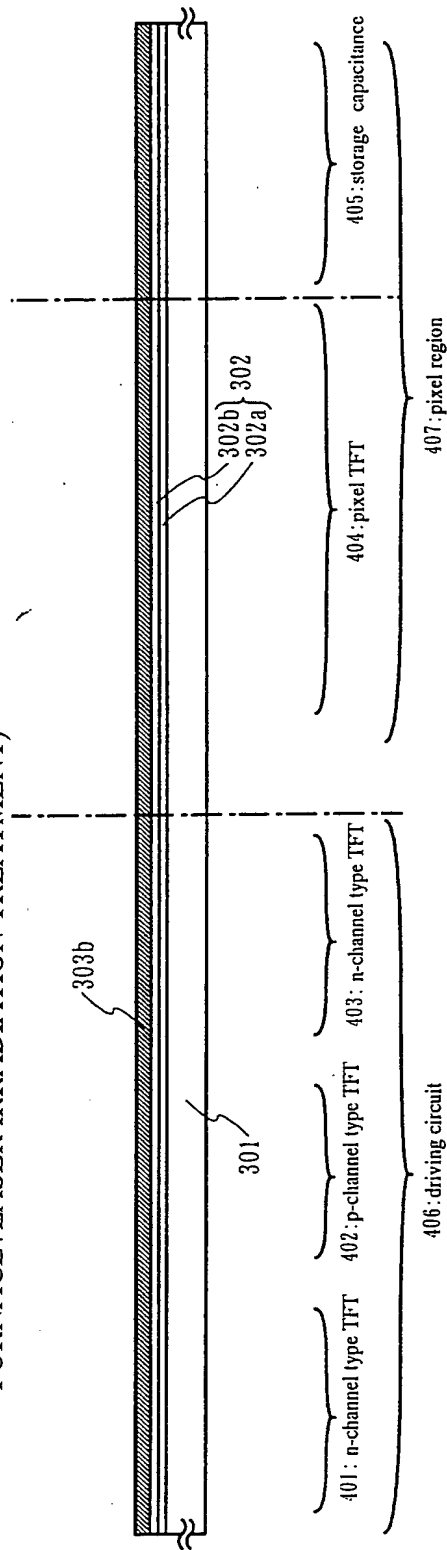
FORMATION OF NiCONTAINING LAYER BY SPIN DRYING ACCELERATION TO 1200 RPM AT LOW ACCELERATION OF 30 RPM/SEC SPIN DRYING FOR 20 SEC AT 1200 RPM

CRYSTALLIZATION OF AMORPHOUS SILICON FILM BY HEAT TREATMENT (HORIZONTAL GROWTH METHOD)

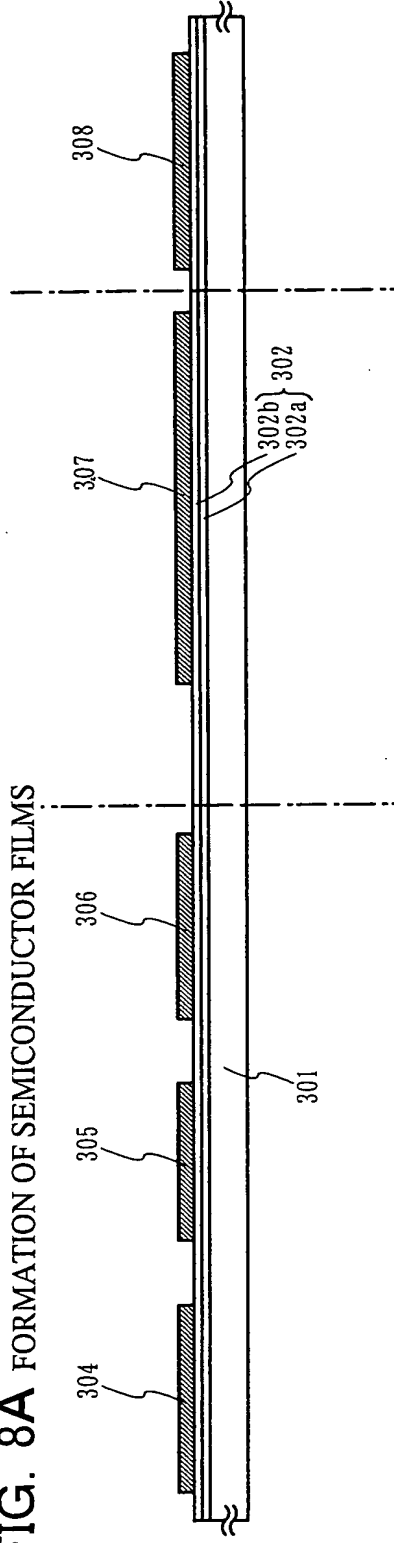
**FIG. 7A** DEPOSITION OF AMORPHOUS SILICON FILM/PREPROCESS/ADDITION OF NI ELEMENT SOLUTION  
ADDITION OF NI ELEMENT SOLUTION



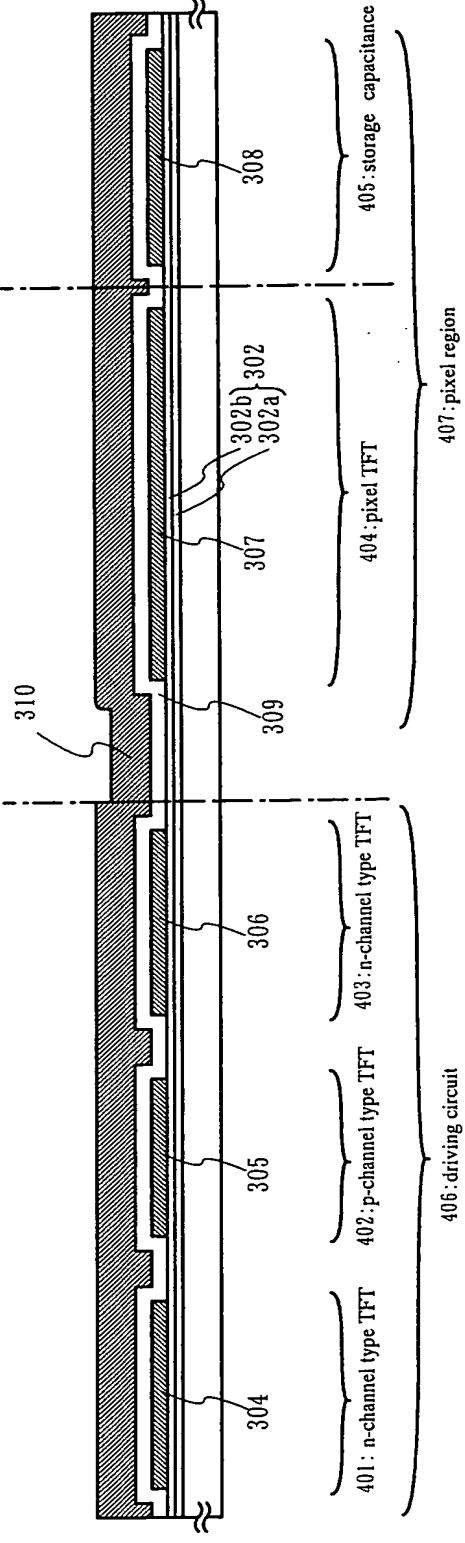
**FIG. 7B** DEHYDROGENATION/ THERMAL CRYSTALLIZATION(HEAT TREATMENT/IN THE ELECTROTHERMAL  
FURNACE+LASER IRRADIATION TREATMENT)



**FIG. 8A** FORMATION OF SEMICONDUCTOR FILMS

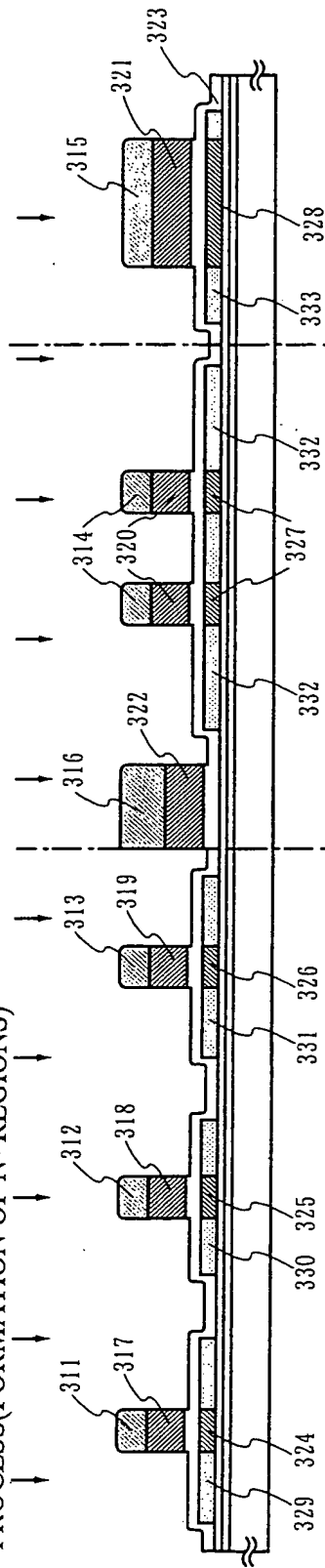


**FIG. 8B** DEPOSITING THE GATE INSULATING FILM/DEPOSITING THE GATE ELECTRODE

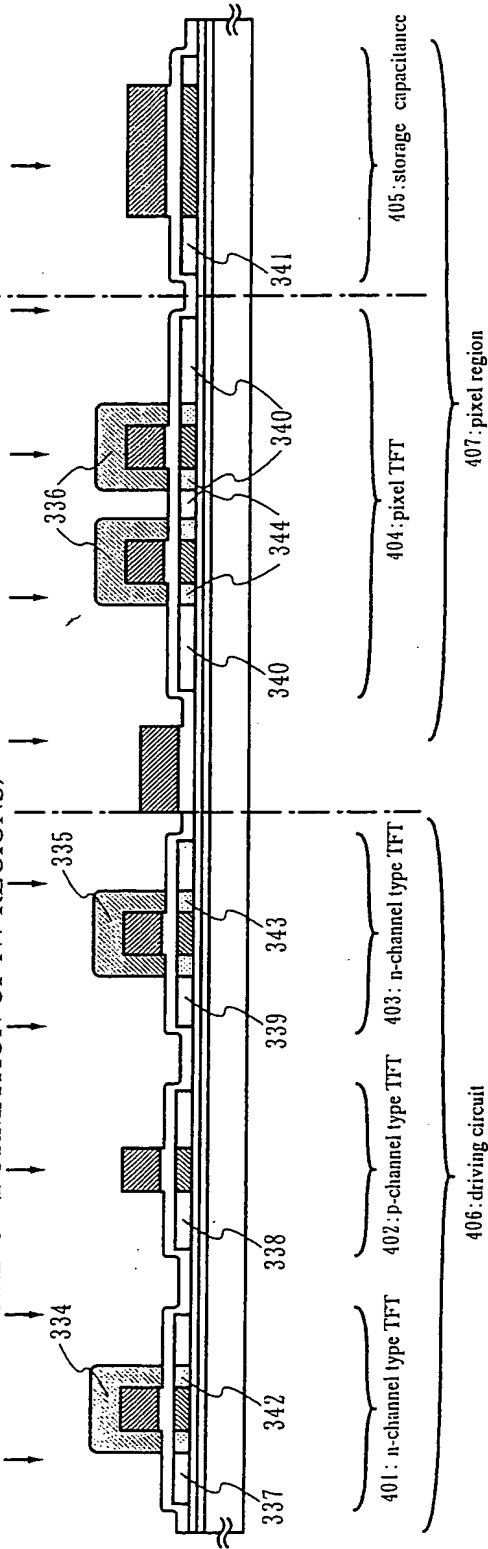




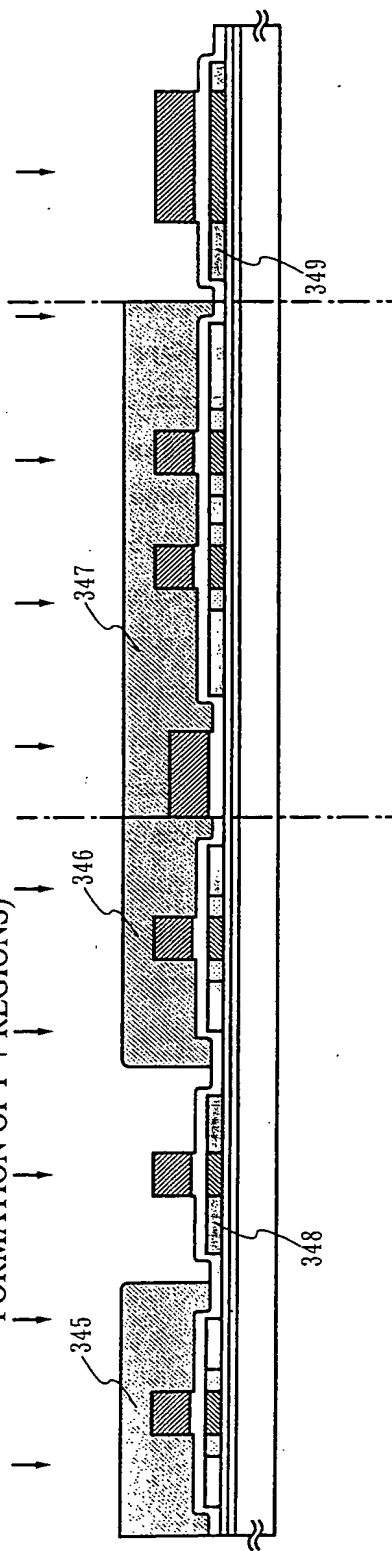
**FIG. 9A** FORMATION OF RESIST PATTERNS FOR GATE ELECTRODES/DRY ETCHING/FIRST ION DOPING PROCESS(FORMATION OF N- REGIONS)



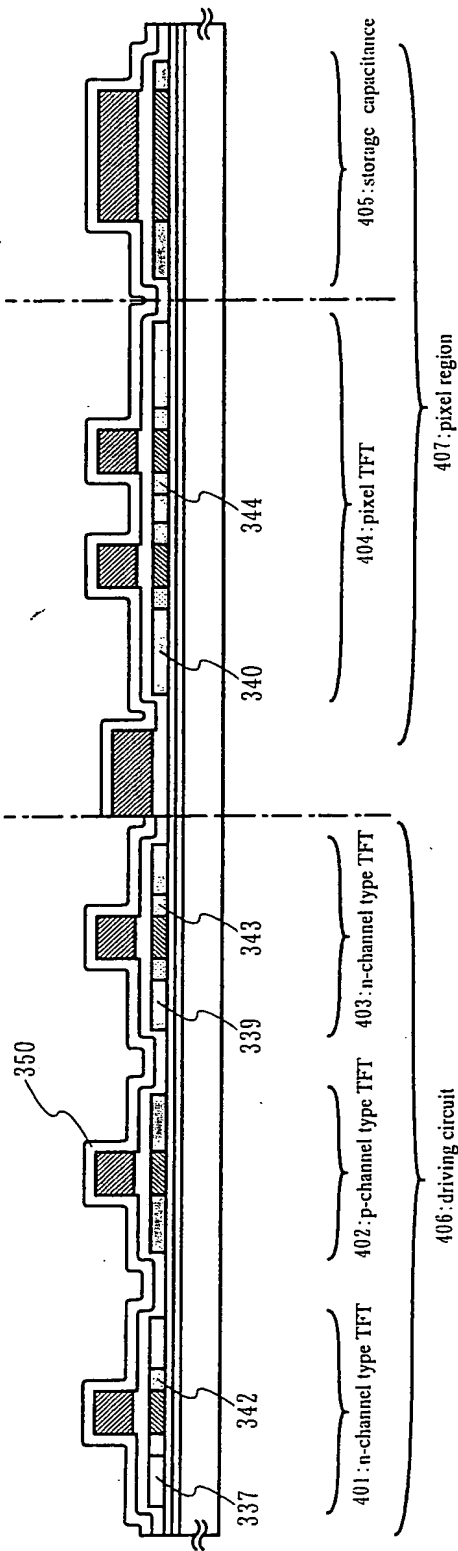
**FIG. 9B** REMOVAL OF MASKS/FORMATION OF RESIST PATTERNS FOR N+ REGIONS/  
SECOND ION DOPING (FORMATION OF N+ REGIONS)



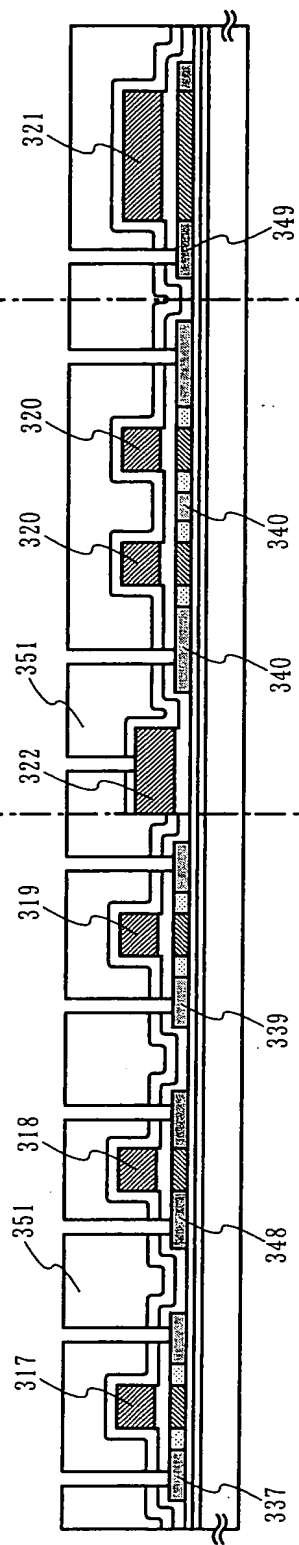
**FIG. 10A** REMOVAL OF MASKS(FORMATION OF RESIST PATTERNS FOR P+ REGIONS/THIRD ION DOPING/FORMATION OF P+ REGIONS)



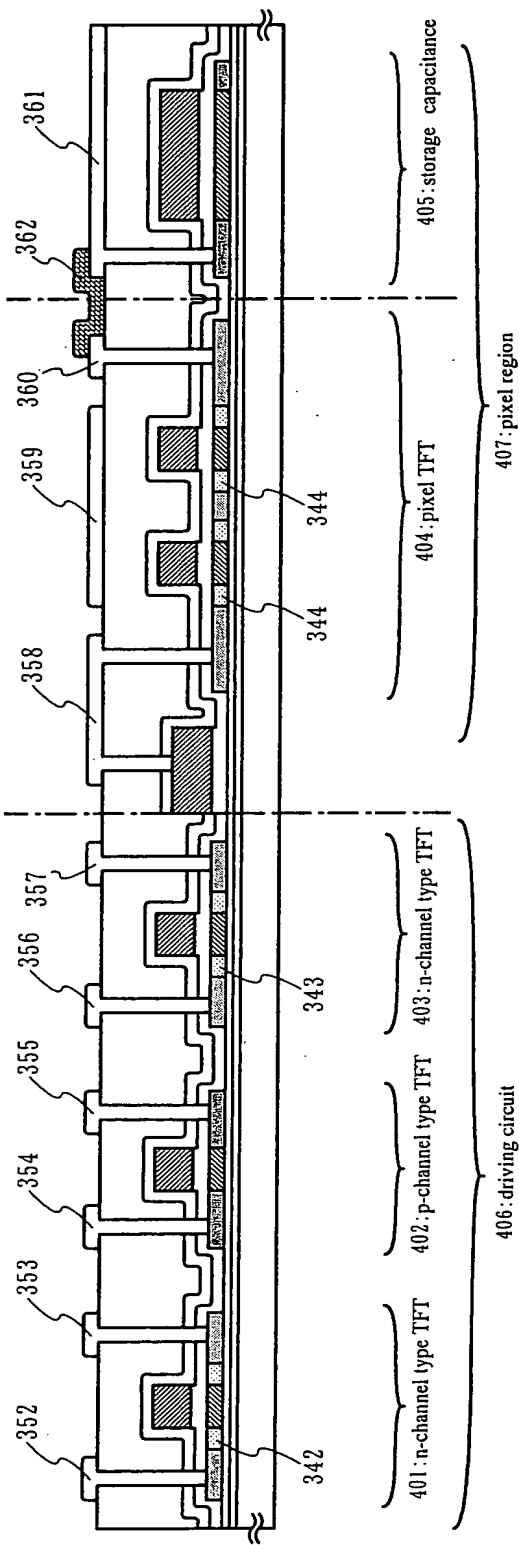
**FIG. 10B** REMOVAL OF RESIST/DEPOSITION OF FIRST INTERLAYER INSULATING FILM/THERMAL ACTIVATION



**FIG. 11A** DEPOSITION OF THE SECOND INTERLAYER INSULATING FILM /FORMATION OF CONTACT HOLES



**FIG. 11B** FORMATION OF METAL WIRINGS /FORMATION OF TRANSPARENT CONDUCTIVE FILM



405: driving circuit

401: n-channel type TFT 402: p-channel type TFT 403: n-channel type TFT

404: pixel TFT

405: storage capacitance

407: pixel region

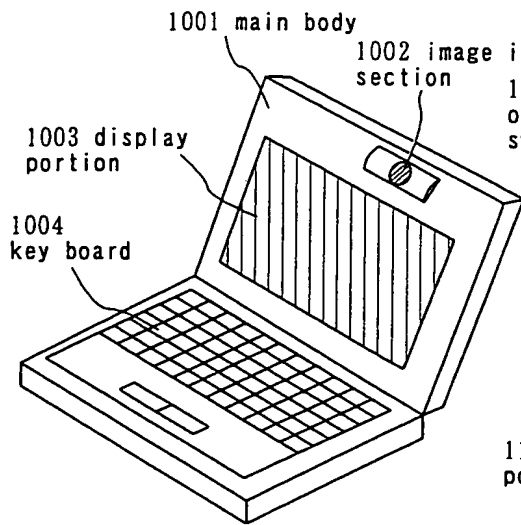


FIG. 12A

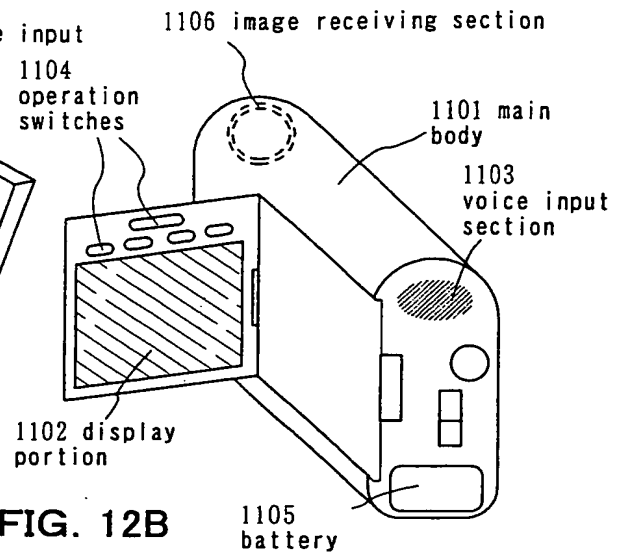


FIG. 12B

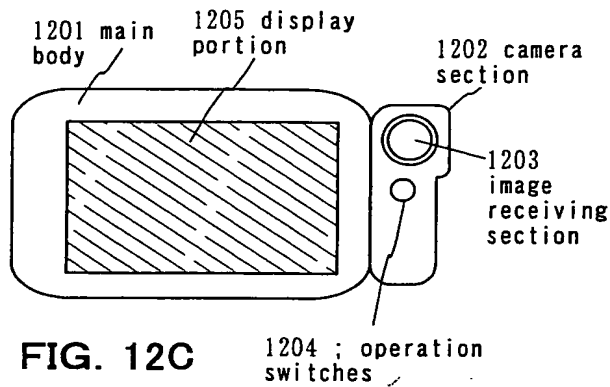


FIG. 12C

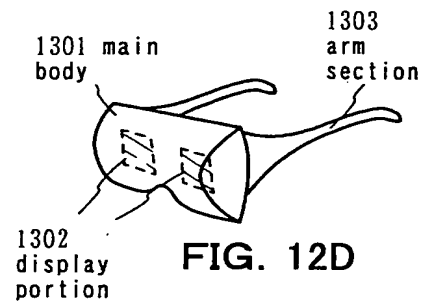


FIG. 12D

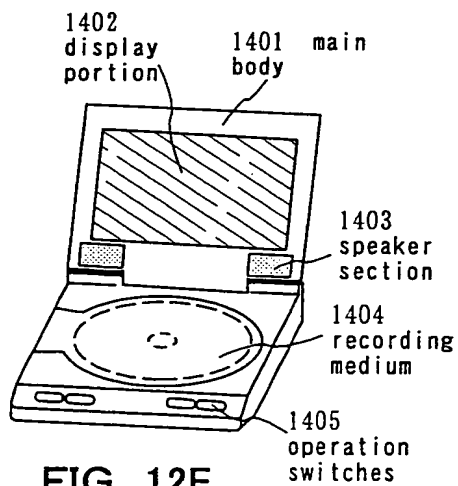


FIG. 12E

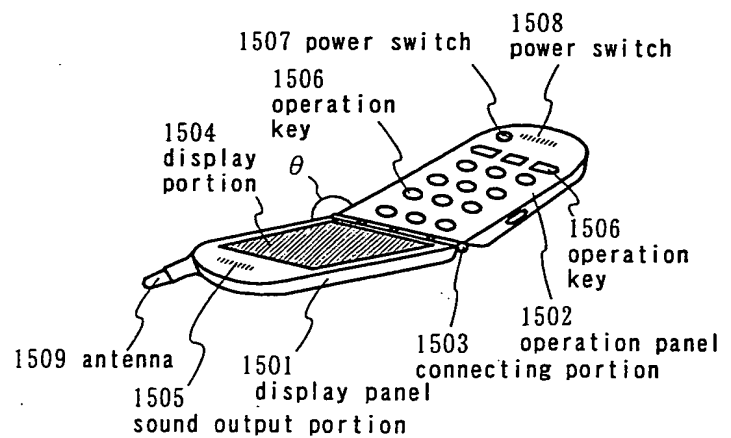


FIG. 12F

FIG. 13A

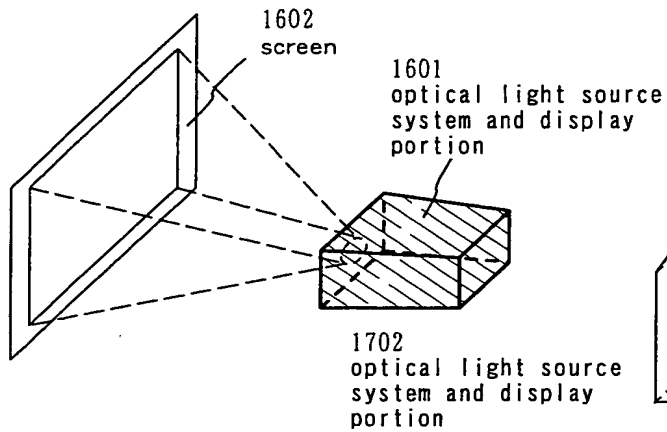


FIG. 13B

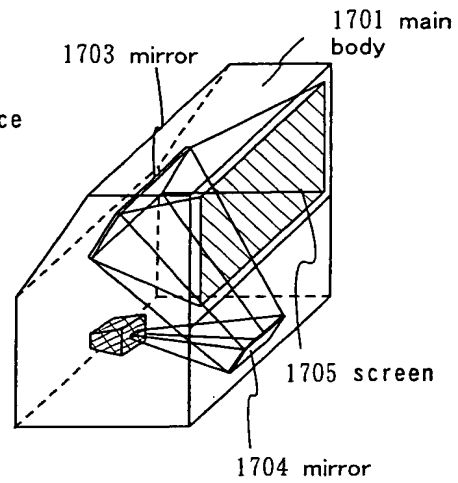


FIG. 13C

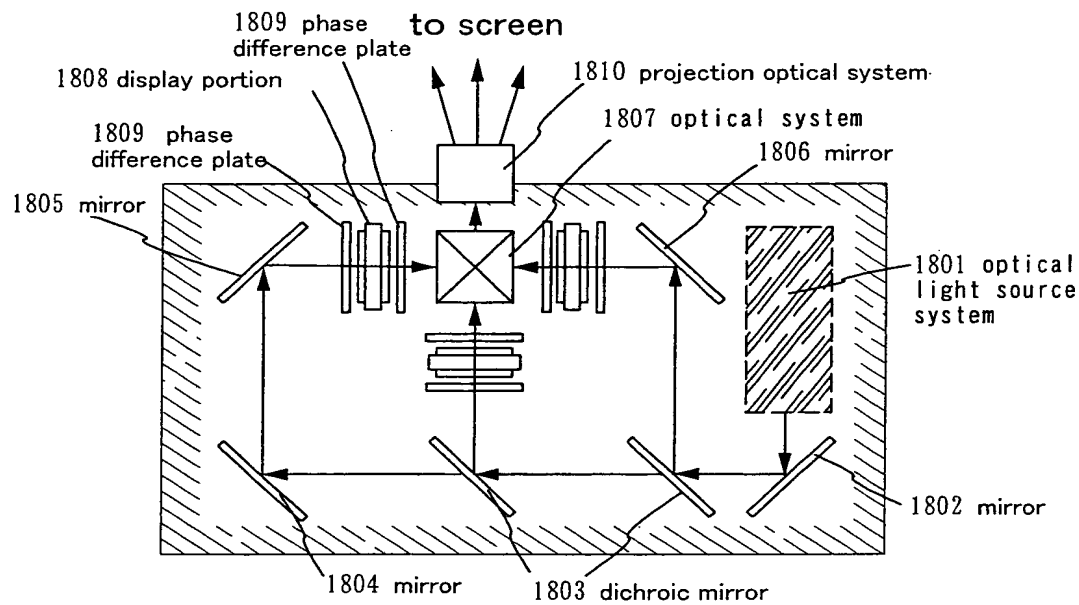
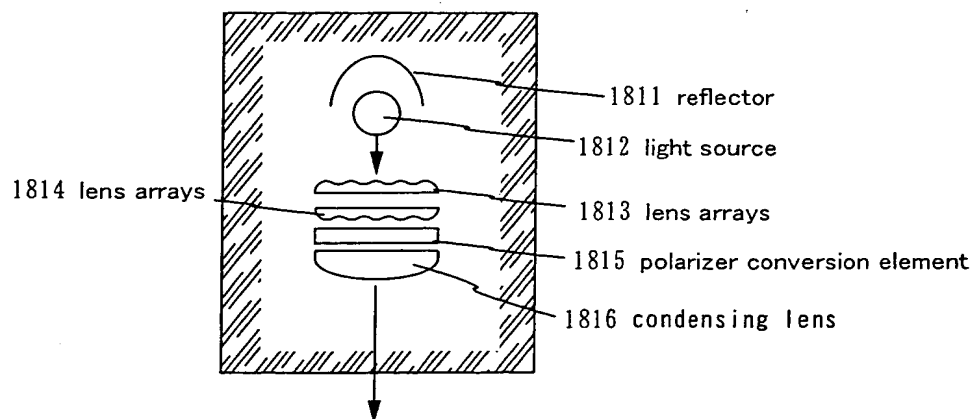
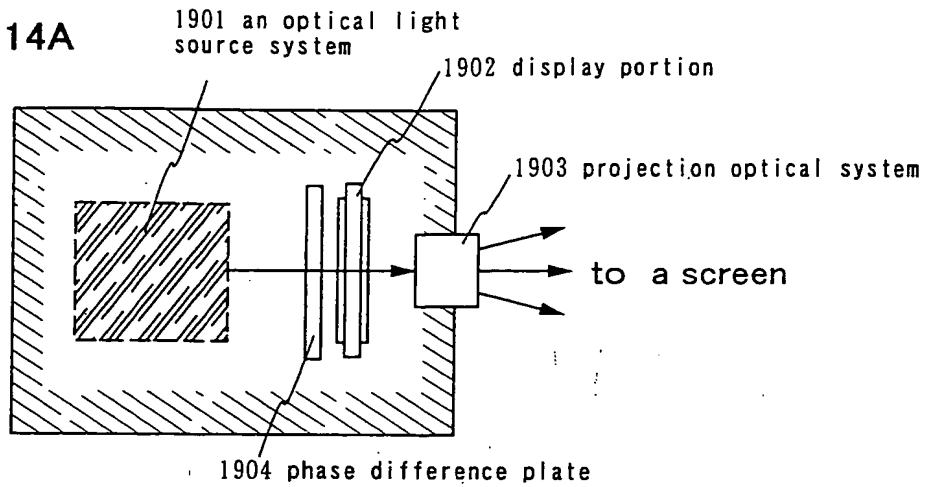


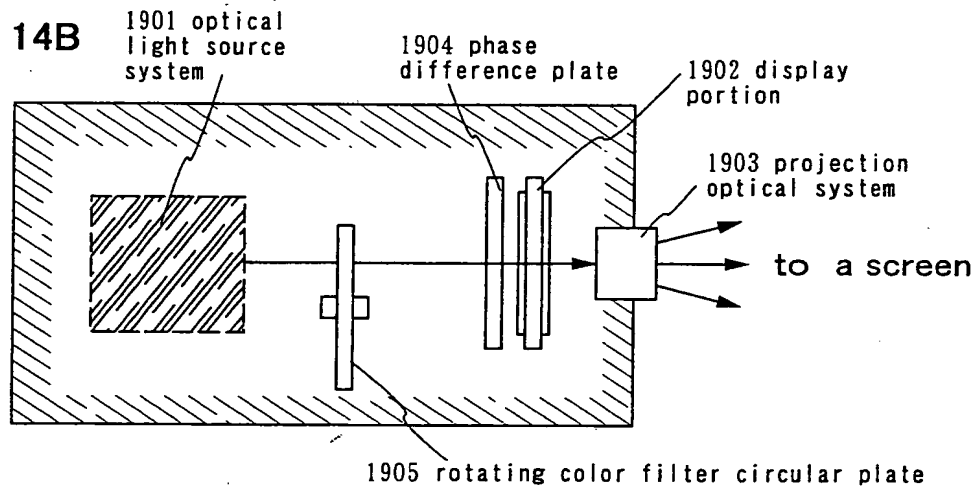
FIG. 13D



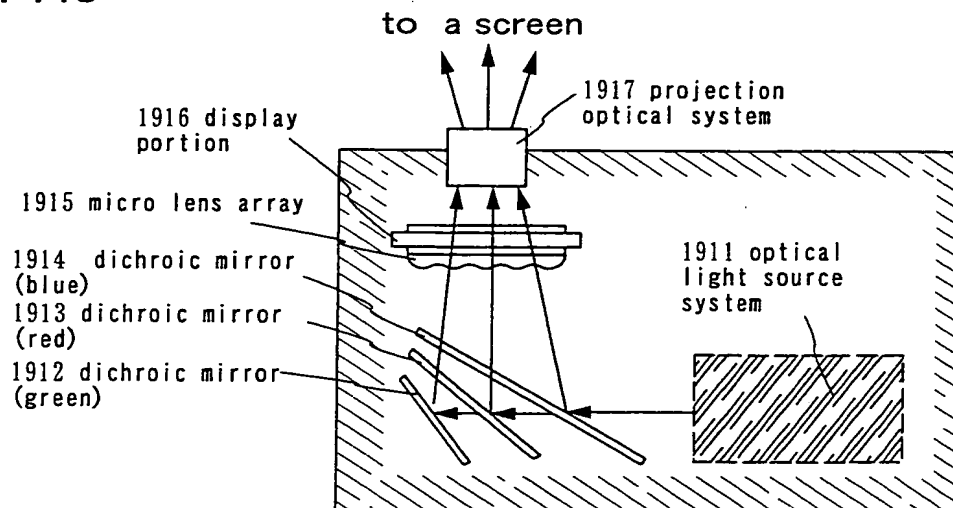
**FIG. 14A**



**FIG. 14B**

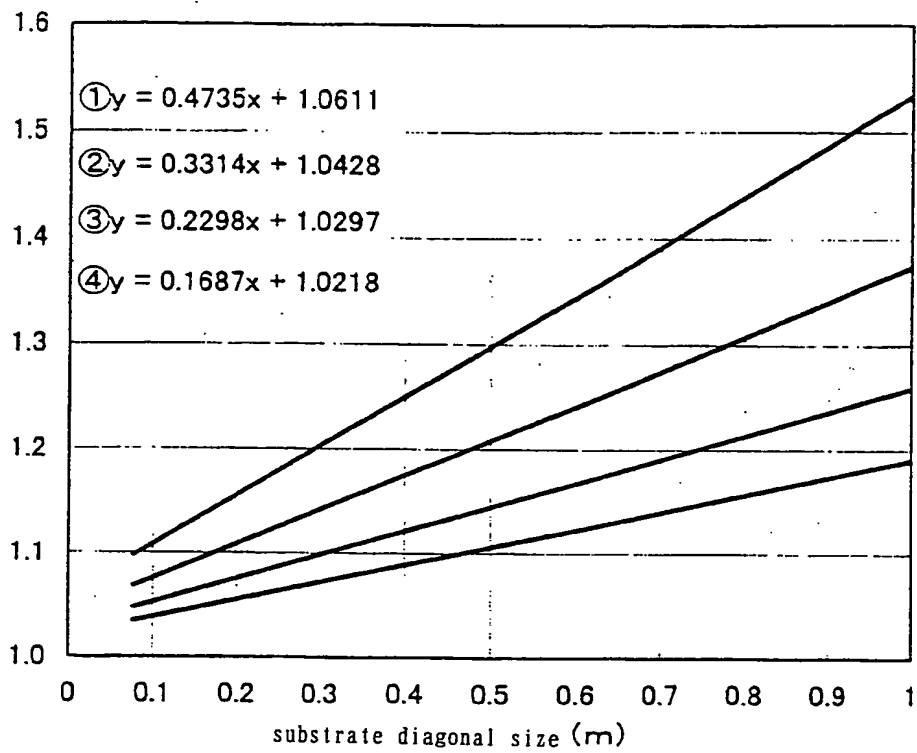


**FIG. 14C**

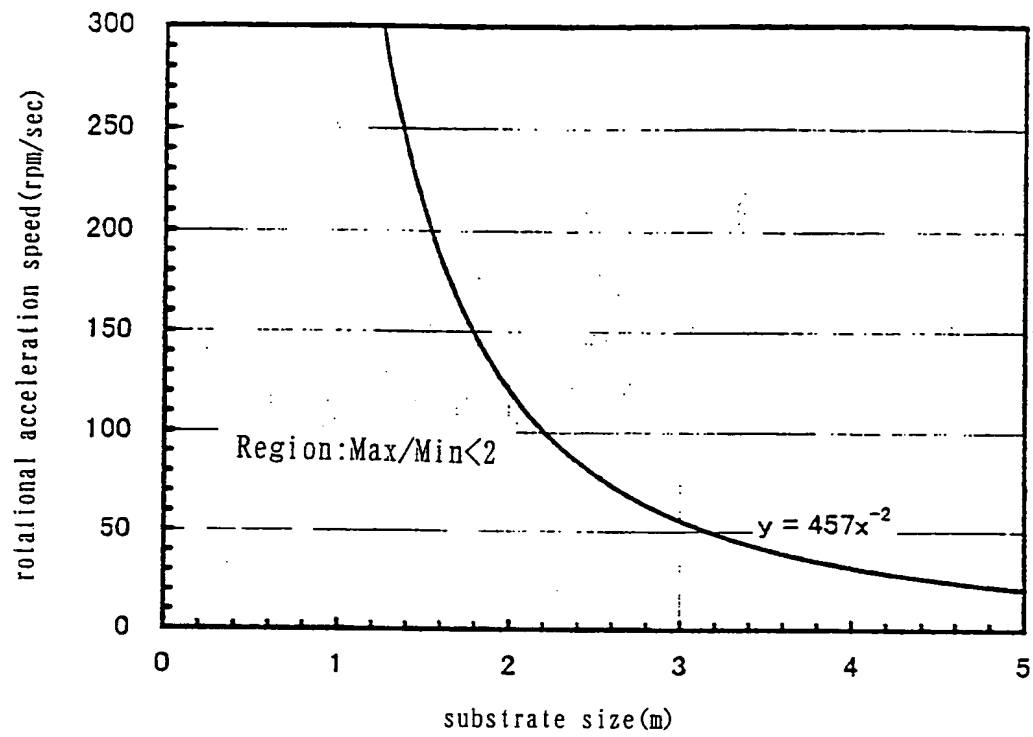


# FIG. 15

Ni concentration ratio between a center portion of the substrate and an edge portion of the substrate, with the concentration at the center portion as one



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the relationship between substrate size and rotational acceleration speed.